



SPECIFICATION OF UT ELECTRONICS

TECHNICAL FEATURES

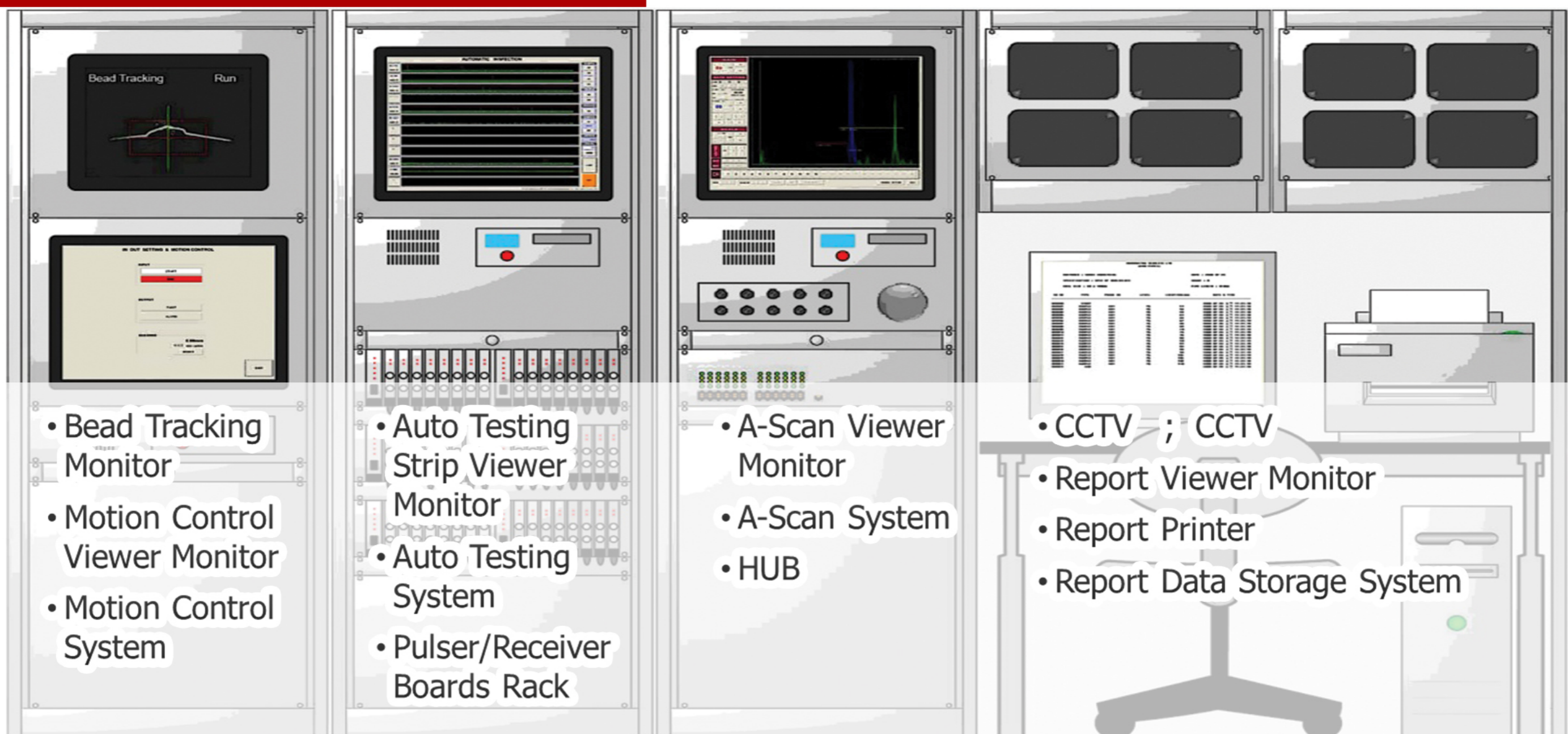
- FPGA + DSP Chip
- Dual Display for A-Scan
- Max.128 channel A-Scan
- Remote Monitor Display
- Noise Protection Circuit
- Up to 20K PRF

OPTIONAL FUNCTION

- Pulse Width : 60 ns to 1 ms
- Damping : 500, or 50 ohms
- PRF : 20KHz
- Gain : 0 dB to 80 dB in 0.1 step
- Filter : 0.5 to 25 MHz
- Waveform Display : FW, HW or RF
- Trigger Source : External, Software
- Gate : Independent 3 gates
- Distance gain correction : DAC Curve
- I / O : 32 I/O
- Ethernet : 10/100 Ethernet



MEET TO MOST STANDARDS



For immediate assistance

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| <ul style="list-style-type: none"> • Gerrad Kim / Senior Engineer - E-mail : gerrad@unitekeng.co.kr - Mobile : +82-10-8962-2279 - Linkedin : paulsgood@gmail.com | <ul style="list-style-type: none"> • Mike Lee / Director - E-mail : mike@unitekeng.co.kr - Mobile : +82-10-3767-1914 - Linkedin : ndt.myungho@gmail.com |
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Headquarter

Seokyoung bldg #1000-3, Daechi-Dong
Gangnam-gu, Seoul, 135-280, Korea
Tel : +82-2-567-0888
Fax : +82-2-3453-1177